## UTILITY PATENT APPLICATION TRANSMITTAL g new nonprovisional applications under 37 CFR 1.53(b))

Attorney Docket No. 250798US2S

Title

First Inventor or Application Identifier | Shinichiro SHIRATAKE, et al.

SEMICONDUCTOR INTEGRATED	CIRCUIT DEVICE	AND OPERATION	METHOD
HEREFOR			<u>ا</u> ا

APPLICATION ELEMENTS See MPEP chapter 600 concerning utility patent application contents	Commissioner for Patents  ADDRESS TO: Mail Stop Patent Application Alexandria, Virginia 22313					
Fee Transmittal Form (e.g. PTO/SB/17)     (Submit an original and a duplicate for fee processing)	ACCOMPANYING APPLICATION PARTS  7. □ Assignment Papers (cover sheet & document(s))					
2. Specification Total Sheets 30	8. Application Data Sheet. See 37 CFR 1.76					
2. Specification Total officets 30.	9. So 37 C.F.R. §3.73(b) Statement Power of Attorney					
3. Drawing(s) (35 U.S.C. 113) Total Sheets 12	10. ☐ English Translation Document (if applicable)					
3.77	11.  Information Disclosure Statement (IDS)/PTO-1449 Copies of IDS Citations					
4.  Oath or Declaration Total Pages	12. Preliminary Amendment					
a.  Newly executed (original or copy)	13. White Advance Serial No. Postcard					
b. Copy from a prior application (37 C.F.R. §1.63(d))	14.   Certified Copy of Priority Document(s)  (if foreign priority is claimed)					
<ul> <li>i. DELETION OF INVENTOR(S)         Signed statement attached deleting inventor(s) named in the prior application, see 37 C.F.R. §1.63(d)(2) and 1.33(b).     </li> </ul>	15.   Applicant claims small entity status.  See 37 CFR 1.27					
	CD-ROM or CD-R in duplicate, large table or Computer 16.   Other:					
6. U Nucleotide and/or Amino Acid Sequence Submission (if applicable, all necessary)						
a.						
b. Specification or Sequence Listing on :						
i. CD-ROM or CD-R (2 copies); or						
ii. 🔲 Paper						
c.  Statements verifying identity of above copies	•					
17. If a CONTINUING APPLICATION, check appropriate box, and sup	ply the requisite information below:					
☐ Continuation ☐ Divisional ☐ Continuation						
Prior application information: Examiner:	Group Art Unit:					
For CONTINUATION OR DIVISIONAL APPS only: The entire disclosure of the prior application, from which an oath or declaration is supplied under Box 4b, is considered a part of the accompanying continuation or divisional application and is hereby incorporated by reference. The incorporation can only be relied upon when a portion has been inadvertently omitted from the submitted application parts.						
18. CORRESPONDENCE ADDRESS						
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D<u>eek</u>et No.

250798US2S

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

NWENTOR(S) Shinichiro SHIRATAKE, et al.

SERIAL NO:

**New Application** 

FILING DATE: Herewith

FOR:

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND OPERATION METHOD THEREFOR

## FEE TRANSMITTAL

COMMISSIONER FOR PATENTS ALEXANDRIA, VIRGINIA 22313

FOR	NUMBER FILED	NUMBER EXTRA	RATE	CALCULATIONS
TOTAL CLAIMS	16 - 20 =	0	x \$18 =	\$0.00
INDEPENDENT CLAIMS	2 - 3 =	0	x \$86 =	\$0.00
☐ MULTIPLE DEPENDENT CLAIMS (If applicable)			+ \$290 =	\$0.00
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•	Respectfully Submitted,
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